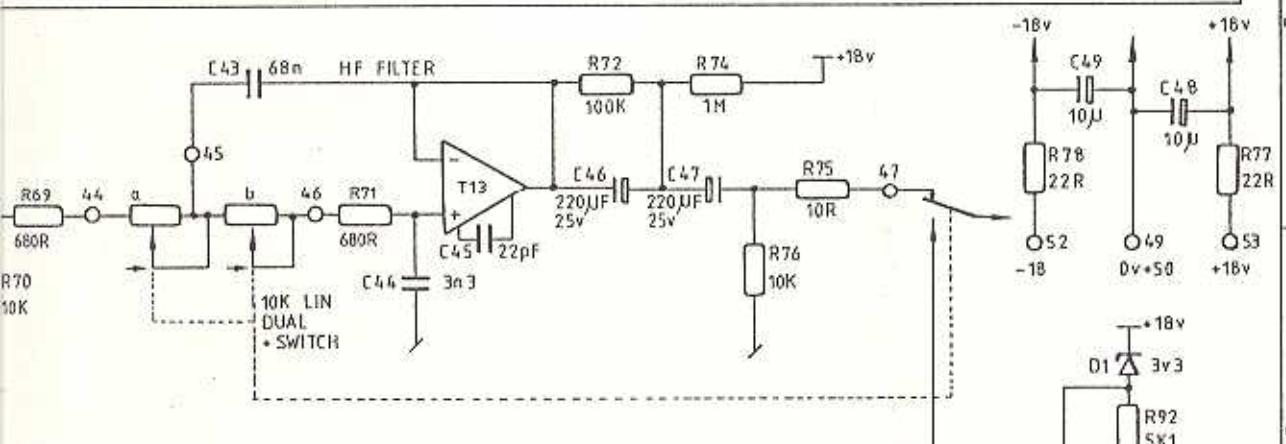
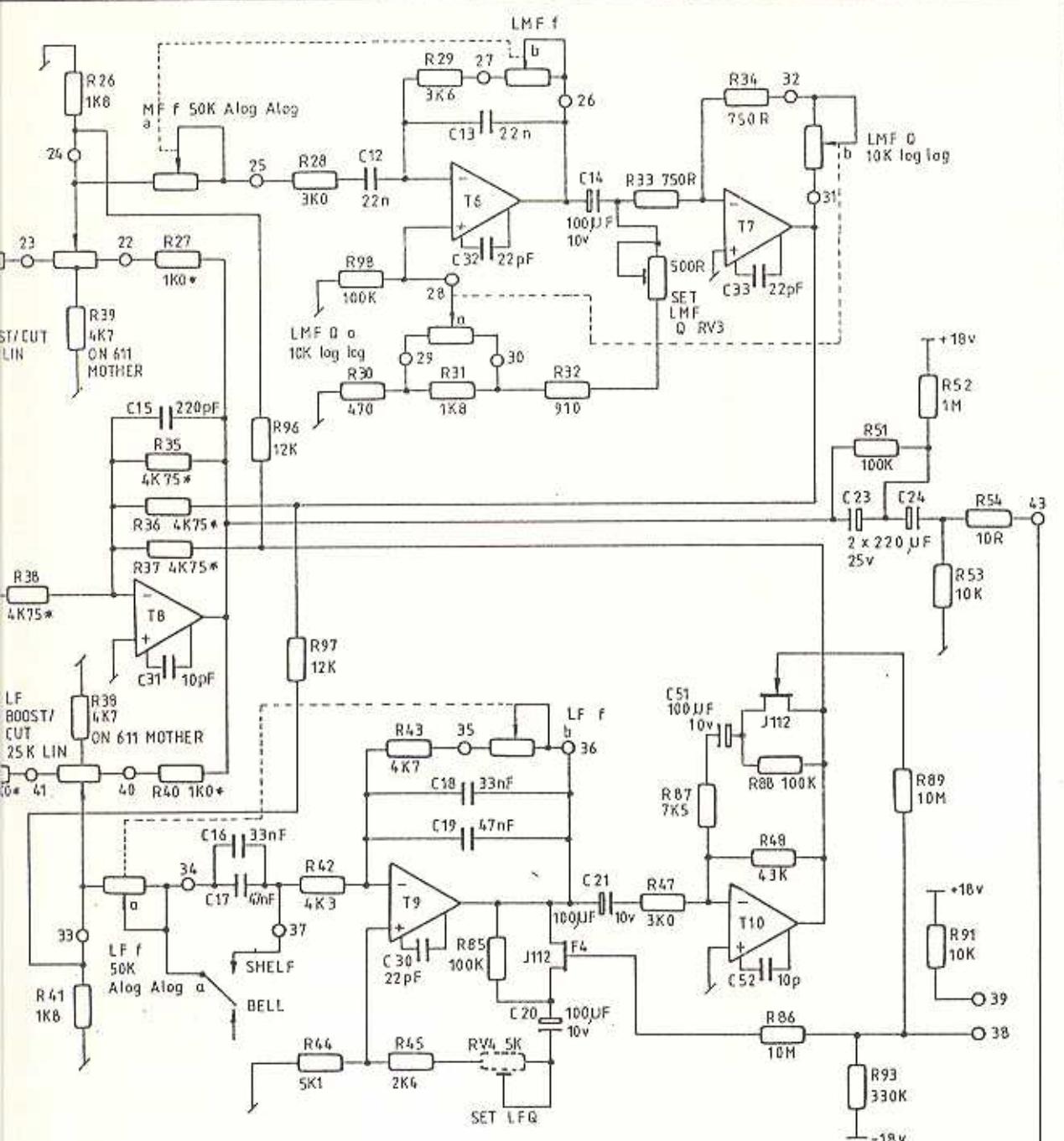


Rev.	Date	Chkd	Details
0			PROTOTYPE
1	3.85		REDRAWN V.B. R7, 9, 45, 48 CHANGED PINS 1 & 2 SWAPPED TR ON PIN 0 NOT 57
2	4.85		FILTER VALUES CHANGED
3	Nov85	QM	R6, 7, 44, 45, 69, 71, 81, 87 RV1, 4 AMENDED
4	Jan86	QA	CAPACITOR ADDED ADJ. R3



PIN 7
10n/63v
PIN 4
10n/63v

ADJACENT T1 THROUGH 13
NUMBERED C53 - 78

NOTES

1. ALL IC'S 5534N
2. * = 0.5% RESISTOR

ASSY REV N°5

Title

EQUALISER FILTERS.

Drg. No.

T82242-70

Solid State Logic